INTEGRATED CIRCUITS

DATA SHEET

TDA10112 to 6 W audio power amplifier

Product specification
File under Integrated Circuits, IC01





2 to 6 W audio power amplifier

TDA1011

The TDA1011 is a monolithic integrated audio amplifier circuit in a 9-lead single in-line (SIL) plastic package. The device is especially designed for portable radio and recorder applications and delivers up to 4 W in a 4 Ω load impedance. The device can deliver up to 6 W into 4 Ω at 16 V loaded supply in mains-fed applications. The maximum permissible supply voltage of 24 V makes this circuit very suitable for d.c. and a.c. apparatus, while the very low applicable supply voltage of 3,6 V permits 6 V applications. Special features are:

- single in-line (SIL) construction for easy mounting
- separated preamplifier and power amplifier
- · high output power
- · thermal protection
- high input impedance
- · low current drain
- limited noise behaviour at radio frequencies

QUICK REFERENCE DATA

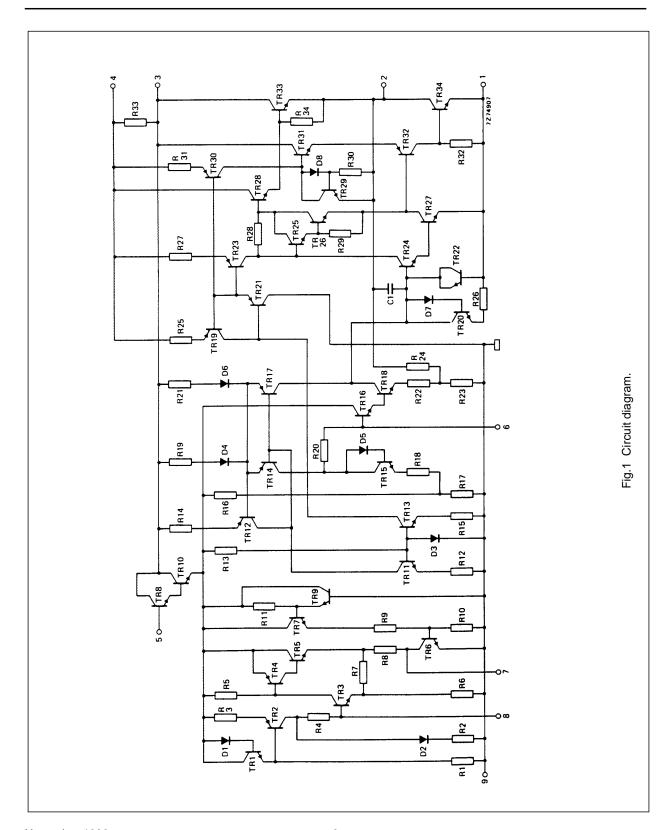
Supply voltage range	V _P	3,6 to 20 V		V
Peak output current	I _{OM}	max.	3	Α
Output power at d _{tot} = 10%				
$V_P = 16 \text{ V}; R_L = 4 \Omega$	Po	typ.	6,5	W
$V_P = 12 \text{ V}; R_L = 4 \Omega$	P _o	typ.	4,2	W
$V_P = 9 V; R_L = 4 \Omega$	P _o	typ.	2,3	W
$V_P = 6 V; R_L = 4 \Omega$	P _o	typ.	1,0	W
Total harmonic distortion at $P_0 = 1$ W; $R_L = 4$ Ω	d_{tot}	typ.	0,2	%
Input impedance				
preamplifier (pin 8)	$ Z_i $	> ′	100	$k\Omega$
power amplifier (pin 6)	$ Z_i $	typ.	20	$k\Omega$
Total quiescent current	I _{tot}	typ.	14	mA
Operating ambient temperature	T_{amb}	-25 to + 150		°C
Storage temperature	T_{stg}	-55 to +1	150	°C

PACKAGE OUTLINE

9-lead SIL; plastic (SOT110B); SOT110-1; 1996 July 23.

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RATINGS

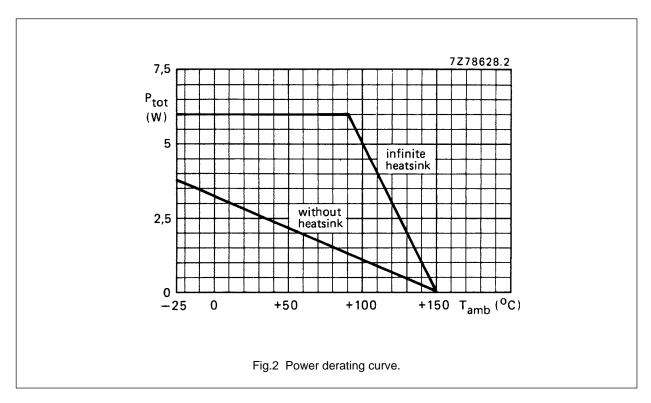
Limiting values in accordance with the Absolute Maximum System (IEC 134)

Supply voltage V_P max. 24 V_P Peak output current I_{OM} max. 3 A Total power dissipation see derating curve Fig.2

Storage temperature T_{stg} -55 to + 150 °C Operating ambient temperature T_{amb} -25 to + 150 °C

A.C. short-circuit duration of load

during sine-wave drive; $V_P = 12 \text{ V}$ t_{SC} max. 100 hours



HEATSINK DESIGN

Assume $V_P = 12 \text{ V}$; $R_L = 4 \Omega$; $T_{amb} = 60 \, ^{\circ}\text{C}$ maximum; $P_0 = 3.8 \, \text{W}$.

The maximum sine-wave dissipation is 1,8 W.

The derating of 10 K/W of the package requires the following external heatsink (for sine-wave drive):

$$R_{th j-a} = R_{th j-tab} + R_{th tab-h} + R_{th h-a} = \frac{150 - 60}{1, 8} = 50 \text{ K/W}.$$

Since $R_{th\ j\text{-tab}}$ = 10 K/W and $R_{th\ tab\text{-}h}$ = 1 K/W, $R_{th\ h\text{-}a}$ = 50 - (10 + 1) = 39 K/W.

Product specification Philips Semiconductors

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22 mA

Supply voltage range	V_{P}	3,6	to 20 V	
Repetitive peak output current	I_{ORM}	<	2 A	
Total guiescent current at V _P = 12 V	ltat	typ.	14 mA	

Total quiescent current at $V_P = 12 \text{ V}$

A.C. CHARACTERISTICS

D.C. CHARACTERISTICS

 T_{amb} = 25 °C; V_P = 12 V; R_L = 4 Ω ; f = 1 kHz unless otherwise specified; see also Fig.3.

A.F. output power at $d_{tot} = 10\%$ (note 1)

7				
with bootstrap:				
$V_P = 16 \text{ V}; R_L = 4 \Omega$	P_{o}	typ.	6,5	W
$V_{P} = 12 \text{ V}; R_{I} = 4 \Omega$	D	>	3,6	W
$V_{p} = 12 \text{ V}, K_{L} = 4.52$	P _o	typ.	4,2	W
$V_P = 9 V; R_L = 4 \Omega$	P_{o}	typ.	2,3	W
$V_P = 6 V; R_L = 4 \Omega$	P_{o}	typ.	1,0	W
without bootstrap:				
$V_P = 12 \text{ V}; R_L = 4 \Omega$	P_{o}	typ.	3,0	W
Voltage gain:				
annound for (acts 2)	0	typ.	23	dB
preamplifier (note 2)	G _{v1}	21	to 25	dB
nower amplifier	0	typ.	29	dB
power amplifier	G_{v2}	27	3,6 W 0. 4,2 W 0. 2,3 W 0. 1,0 W 0. 3,0 W 0. 3,0 W 0. 23 dB 21 to 25 dB 0. 29 dB 27 to 31 dB 0. 52 dB 50 to 54 dB 0. 52 dB 0. 1 % 0 Hz to 15 kHz 100 kΩ 0. 200 kΩ 0. 20 kΩ 0. 20 kΩ	
total amplifica	0	typ.	52	dB
total amplifier	$G_{v tot}$	50	to 54	dB
Total harmonic distantion at D. 4.5 W.	al	typ.	0,3	%
Total harmonic distortion at P _o = 1,5 W	d _{tot}	<	1	%
Frequency response; –3 dB (note 3)	В	60 Hz	to 15	kHz
Input impedance:				
nanamalifian (nata 4)	17 1	>	100	$k\Omega$
preamplifier (note 4)	Z _{i1}	typ.	200	$k\Omega$
power amplifier	Z _{i2}	typ.	20	$k\Omega$
Output impedance preamplifier	Z _{o1}	typ.	1	$k\Omega$
Output voltage preamplifier (r.m.s. value)				
d _{tot} < 1% (note 2)	V _{o(rms)}	>	0,7	V
Noise output voltage (r.m.s. value; note 5)	, -/			
B				

 $R_S = 0 \Omega$

 $R_S = 10 \text{ k}\Omega$

B = 5 kHz; $R_S = 0 \Omega$

Noise output voltage at f = 500 kHz (r.m.s. value)

 $V_{n(rms)}$ typ.

 $V_{n(rms)}$ typ.

 $V_{n(rms)}$ typ.

0,2 mV 0,6 mV

1,4 mV

8 μV

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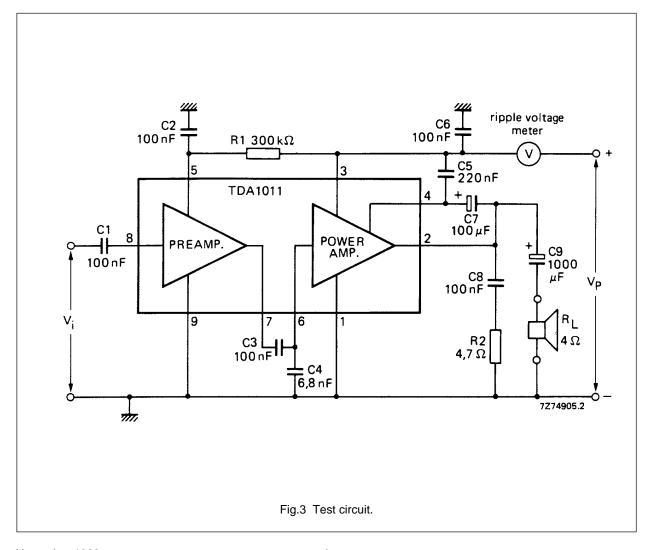
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Ripple rejection (note 6)

f = 1 to 10 kHz	RR	typ.	42	dΒ
$f = 100 \text{ Hz}$; $C2 = 1 \mu\text{F}$	RR	>	35	dΒ
Bootstrap current at onset of clipping; pin 4 (r.m.s. value)	$I_{4(rms)}$	typ.	35	mΑ

Notes

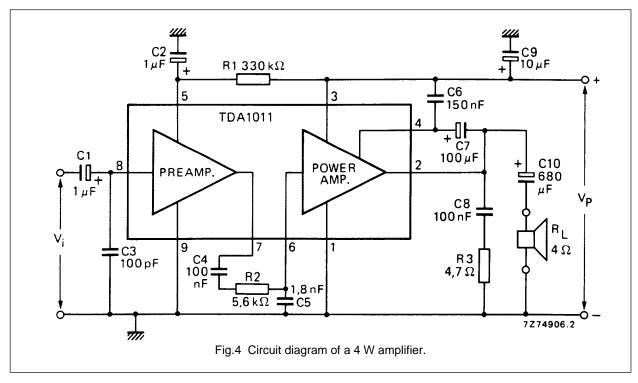
- 1. Measured with an ideal coupling capacitor to the speaker load.
- 2. Measured with a load resistor of 20 $k\Omega$.
- 3. Measured at P₀ = 1 W; the frequency response is mainly determined by C1 and C3 for the low frequencies and by C4 for the high frequencies.
- 4. Independent of load impedance of preamplifier.
- 5. Unweighted r.m.s. noise voltage measured at a bandwidth of 60 Hz to 15 kHz (12 dB/octave).
- Ripple rejection measured with a source impedance between 0 and 2 kΩ (maximum ripple amplitude: 2 V).
- 7. The tab must be electrically floating or connected to the substrate (pin 9).

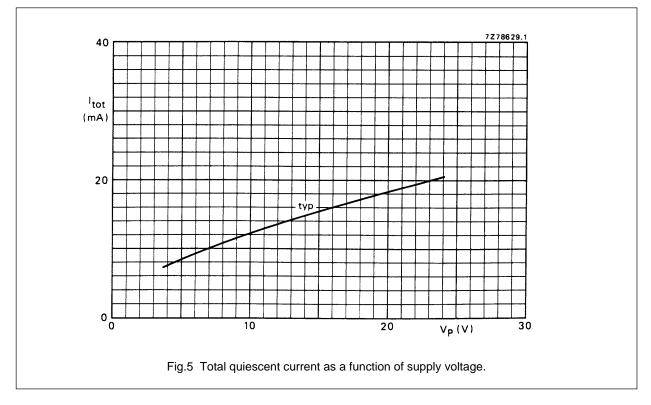


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APPLICATION INFORMATION





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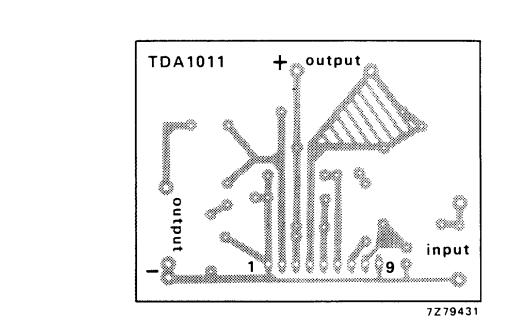
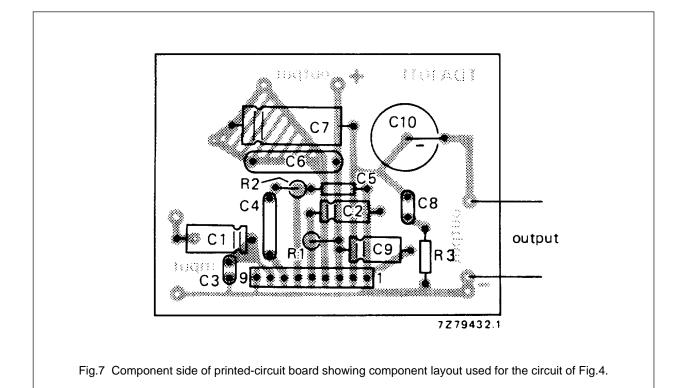


Fig.6 Track side of printed-circuit board used for the circuit of Fig.4; p.c. board dimensions 62 mm \times 48 mm.



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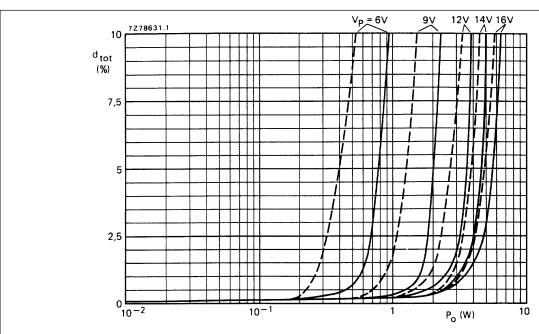


Fig.8 Total harmonic distortion as a function of output power across R_L; _____ with bootstrap; ____ with bootstrap; ___ without bootstrap; f = 1 kHz; typical values. The available output power is 5% higher when measured at pin 2 (due to series resistance of C10).

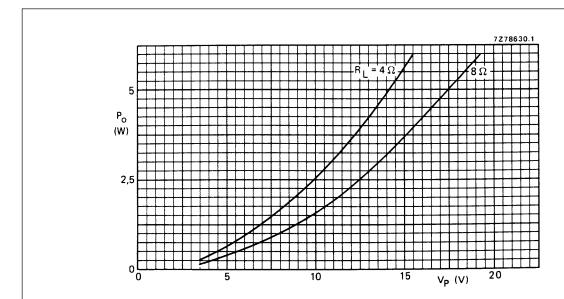


Fig.9 Output power across R_L as a function of supply voltage with bootstrap; $d_{tot} = 10\%$; typical values. The available output power is 5% higher when measured at pin 2 (due to series resistance of C10).

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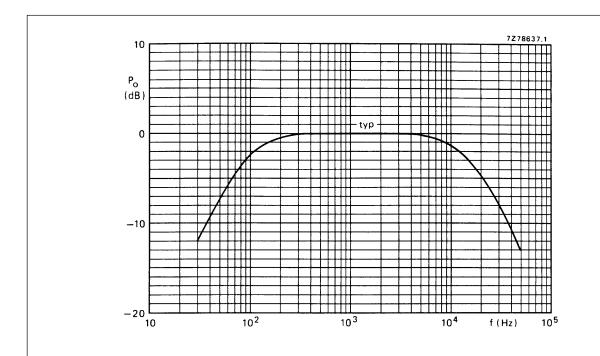


Fig.10 Voltage gain as a function of frequency; P_o relative to 0 dB = 1 W; V_P = 12 V; R_L = 4 Ω .

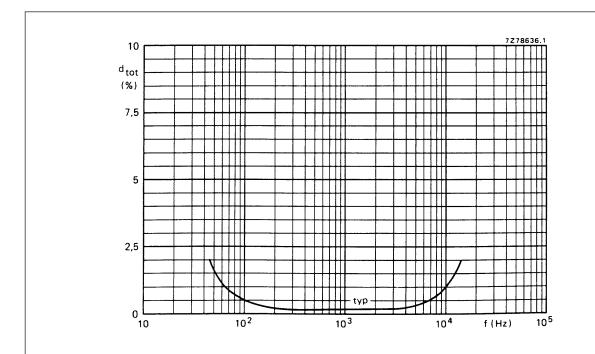
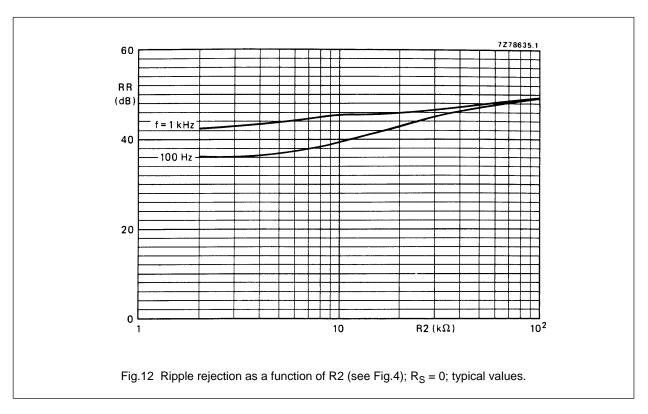


Fig.11 Total harmonic distortion as a function of frequency; P_0 = 1 W; V_P = 12 V; R_L = 4 Ω .

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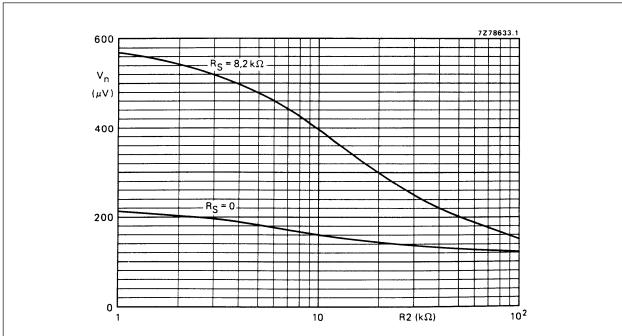


Fig.13 Noise output voltage as a function of R2 (see Fig.4); measured according to A-curve; capacitor C5 is adapted for obtaining a constant bandwidth.

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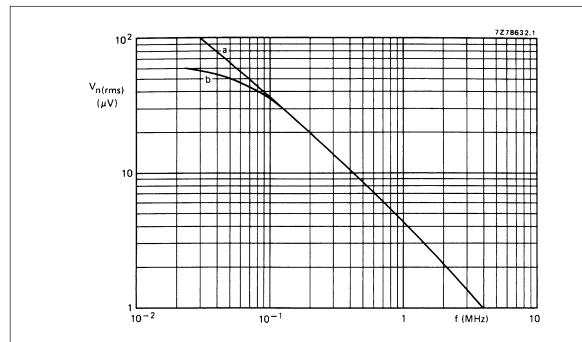
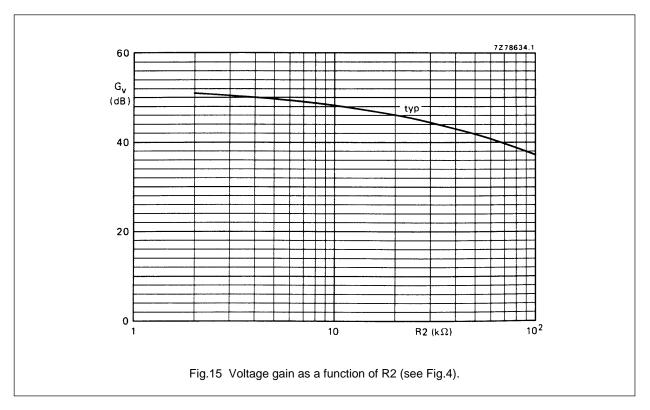


Fig.14 Noise output voltage as a function of frequency; curve a: total amplifier; curve b: power amplifier; B = 5 kHz; $R_S = 0$; typical values.



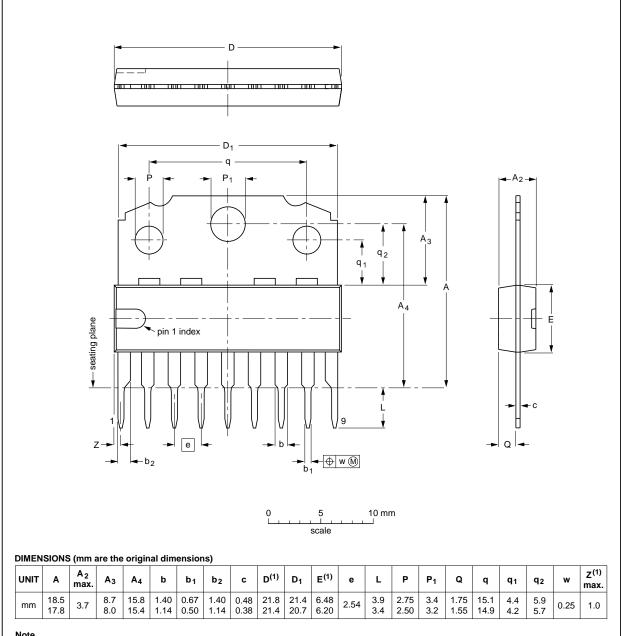
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PACKAGE OUTLINE

SIL9MPF: plastic single in-line medium power package with fin; 9 leads

SOT110-1



Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT110-1						92-11-17 95-02-25

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SOLDERING

Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our "IC Package Databook" (order code 9398 652 90011).

Soldering by dipping or by wave

The maximum permissible temperature of the solder is 260 °C; solder at this temperature must not be in contact with the joint for more than 5 seconds. The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature (T_{stg max}). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

Repairing soldered joints

Apply a low voltage soldering iron (less than 24 V) to the lead(s) of the package, below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than 300 °C it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and 400 °C, contact may be up to 5 seconds.

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.