P-Channel Silicon MOSFET



2SJ307

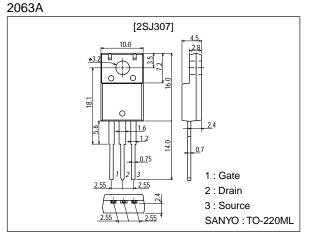
Ultrahigh-Speed Switching Applications

Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.
- · Micaless package facilitating mounting.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-250	V
Gate-to-Source Voltage	V _{GSS}		±30	V
Drain Current (DC)	Ι _D		-6	A
Drain Current (Pulse)	I _{DP}	PW≤10µs, duty cycle≤1%	-24	A
Allowable Power Dissipation	PD		2.0	W
		Tc=25°C	30	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

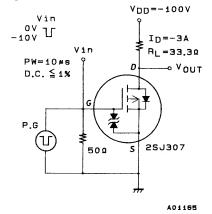
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _{GS} =0	-250			V
Gate-to-Source Breakdown Voltage	V(BR)GSS	I _G =±100μA, V _{DS} =0	±30			V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-250V, V _{GS} =0			-100	μΑ
Gate-to-Souce Leakage Current	IGSS	$V_{GS}=\pm 25V, V_{DS}=0$			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.5		-2.5	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-3A	3	5		S
Static Drain-to-Source On-State Resistance	R _{DS(on)}	I _D =-3A, V _{GS} =-10V		0.75	1.0	Ω

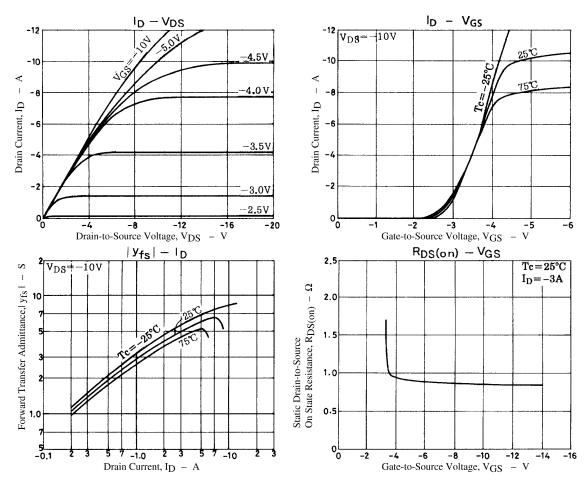
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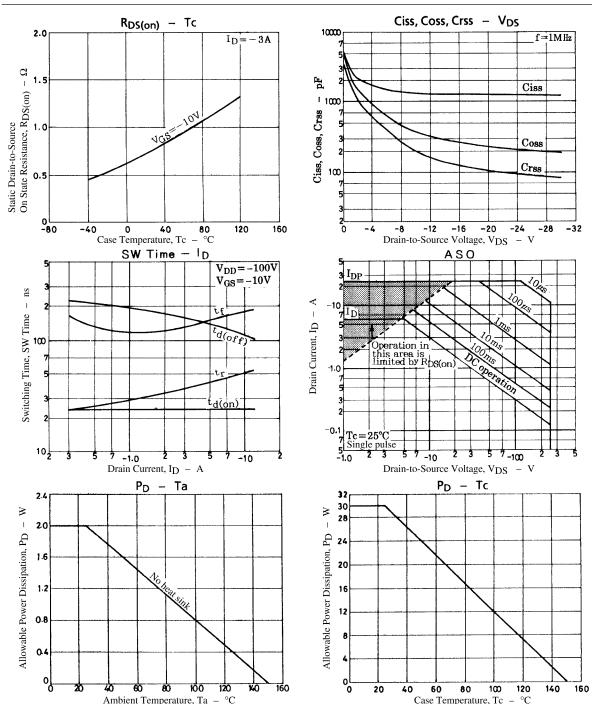
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Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		1250		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		235		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		105		pF
Turn-ON Delay Time	^t d(on)	See specified Test Circuit		24		ns
Rise Time	tr	See specified Test Circuit		37		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		155		ns
Fall Time	t _f	See specified Test Circuit		130		ns
Diode Forward Voltage	V _{SD}	I _S =-6A, V _{GS} =0		-1.0	-1.5	V

Switching Time Test Circuit







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