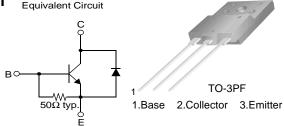


KSC5802D

 $\begin{array}{ll} \textbf{High Voltage Color Display Horizontal} & _{\text{Equivalent Circuit}} \\ \textbf{Deflection Output} & _{\text{C}} \end{array}$

(Built In Damper Diode)

- High Breakdown Voltage BV_{CBO} =1500V
- High Speed Switching : t_F=0.1μs (Typ.)
- · Wide S.O.A
- For C-Monitor(69KHz)



NPN Triple Diffused Planar Silicon Transistor

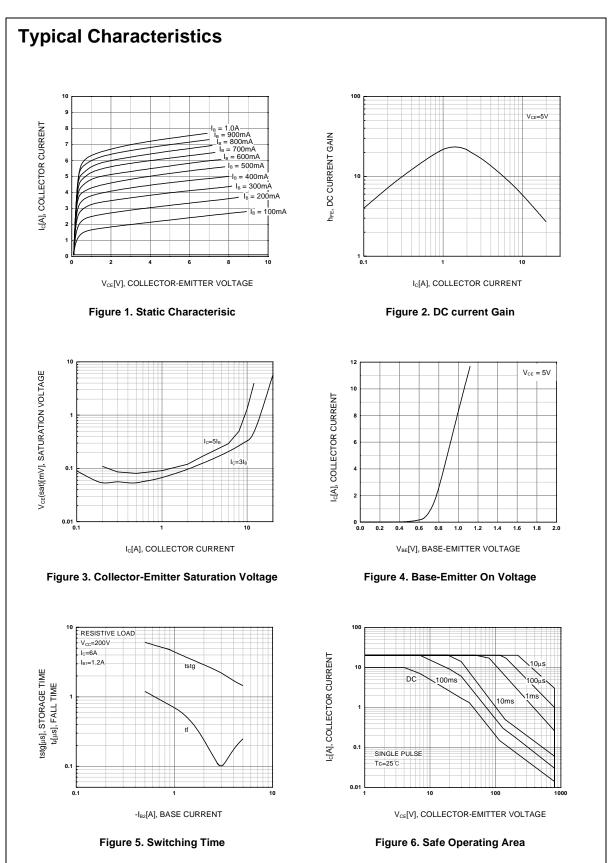
Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	
V _{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	800	V
V_{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	10	Α
I _{CP}	Collector Current (Pulse)	30	Α
P _C	Collector Dissipation (T _C =25°C)	60	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CES}	Collector Cut-off Current	V _{CE} = 1400V, V _{BE} =0			1	mA
I _{CBO}	Collector Cut-off Current	$V_{CB} = 800V, I_{E} = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 4V, I_{C} = 0$	50		250	mA
h _{FE1} h _{FE2}	DC Current Gain	$V_{CE} = 5V, I_{C} = 1A$ $V_{CE} = 5V, I_{C} = 6A$	15 7		40 11.5	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 6A, I_B = 1.5A$			3	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 6A, I_B = 1.5A$			1.5	V
t _F	Fall Time	$V_{CC} = 200V, I_C = 6A$ $I_{B1} = 1.2A, I_{B2} = -2.4A$ $R_L = 33.3\Omega$		0.1	0.3	μѕ

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Typical Characteristics (Continued)

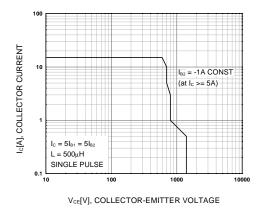


Figure 7. Reverse Bias Safe Operating Area

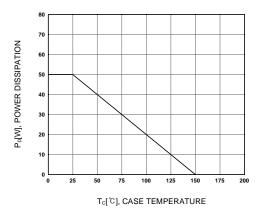
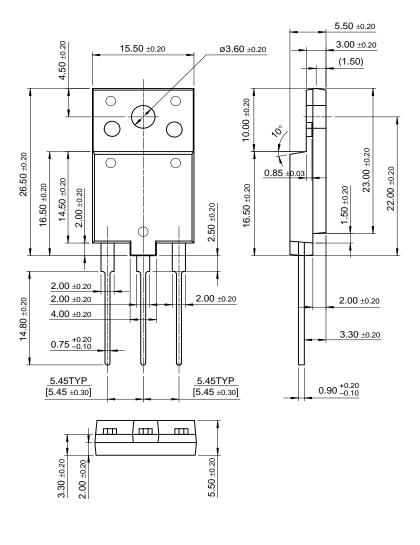


Figure 8. Power Derating

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Package Demensions

TO-3PF



Dimensions in Millimeters

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