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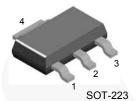


February 2015

# BCP56 NPN General-Purpose Amplifier

## **Description**

These devices are designed for general-purpose medium power amplifiers and switches requiring collector currents to 1 A. Sourced from process 39.



1. Base 2.4. Collector 3. Emitter

## **Ordering Information**

Part Number	Marking	Package	Packing Method
BCP56	BCP56	SOT-223 4L	Tape and Reel

## **Absolute Maximum Ratings**(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	80	V
V <sub>CBO</sub>	Collector-Base Voltage	100	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current - Continuous 1.2		Α
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

### Notes

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

## Thermal Characteristics(3)

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
P <sub>D</sub>	Total Power Dissipation	1	W
	Derate Above 25°C	8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	125	°C/W

## Note:

3. Device is mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead minimum 6 cm<sup>2</sup>.

## **Electrical Characteristics**

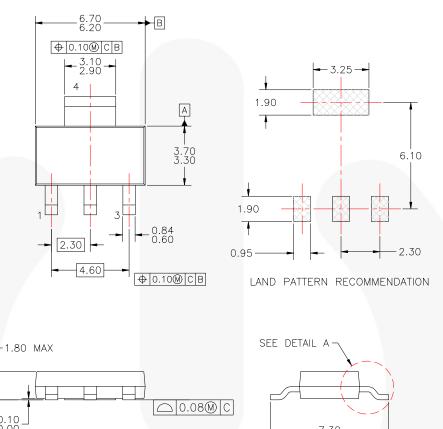
Values are at  $T_A = 25$ °C unless otherwise noted.

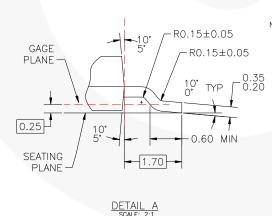
Symbol	Parameter	Conditions	Min.	Max.	Unit
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	80		V
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 100 \mu\text{A}, I_E = 0$	100		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10 \mu A, I_C = 0$	5		V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = 30 V, I <sub>E</sub> = 0		100	nA
		$V_{CB} = 30 \text{ V}, I_{E} = 0, T_{J} = 125^{\circ}\text{C}$		10	μΑ
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 5 \text{ V}, I_{C} = 0$		10	μΑ
h <sub>FE</sub>	DC Current Gain <sup>(4)</sup>	$V_{CE} = 2 \text{ V}, I_{C} = 5 \text{ mA}$	25		
		$V_{CE} = 2 \text{ V}, I_{C} = 150 \text{ mA}$	40	250	
		$V_{CE} = 2 \text{ V}, I_{C} = 500 \text{ mA}$	25		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(4)</sup>	$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		0.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(4)</sup>	$I_C$ = 500 mA, $V_{CE}$ = 2 V		1	V

### Note:

4. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%

## **Physical Dimensions**





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING BASED ON JEDEC REGISTRATION TO-261, VARIATION AA. DIMENSIONS ARE INCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS. ALL DIMENSIONS ARE IN MILLIMETERS.

- ALL DIMENSIONS ARE IN MILLIMETERS DRAWING CONFORMS TO ASME Y14.5M-1994. LANDPATTERN NAME: SOT230P700X180-4BN DRAWING FILENAME: MKT-MA04AREV2

Figure 1. MOLDED PACKAGE, SOT-223, 4-LEAD





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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