# **BT152X series**

### GENERAL DESCRIPTION

Glass passivated thyristors in a full pack, plastic envelope, intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

## **PINNING - SOT186A**

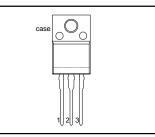
## QUICK REFERENCE DATA

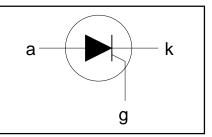
SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V <sub>drm</sub> , V <sub>rrm</sub>	BT152X- Repetitive peak off-state voltages	<b>400R</b> 450	<b>600R</b> 650	<b>800R</b> 800	V
I <sub>T(AV)</sub> I <sub>T(RMS)</sub> I <sub>TSM</sub>	Average on-state current RMS on-state current Non-repetitive peak on-state current	13 20 200	13 20 200	13 20 200	A A A

#### **PIN CONFIGURATION**

### SYMBOL

PIN	DESCRIPTION	
1	cathode	
2	anode	
3	gate	
case	isolated	





### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
V <sub>drm</sub>	Repetitive peak off-state voltages		-	<b>-400R</b> 450 <sup>1</sup>	<b>-600R</b> 650 <sup>1</sup>	<b>-800R</b> 800	V
I <sub>T(AV)</sub> I <sub>T(RMS)</sub> I <sub>TSM</sub>	Average on-state current RMS on-state current Non-repetitive peak on-state current	half sine wave; $T_{hs} \le 43 \text{ °C}$ all conduction angles half sine wave; $T_j = 25 \text{ °C}$ prior to surge	-		13 20		A A
		t = 10  ms t = 8.3 ms	-		200 220		A A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t = 10  ms	-		200		Å <sup>2</sup> s
dl <sub>⊤</sub> /dt	Repetitive rate of rise of on-state current after triggering	$I_{TM} = 50 \text{ A}; I_G = 0.2 \text{ A}; \\ dI_G/dt = 0.2 \text{ A}/\mu s$	-		200		A/μs
I <sub>GM</sub>	Peak gate current		-		5		Α
V <sub>GM</sub>	Peak gate voltage		-		5 5		V
	Peak reverse gate voltage		-				V
P <sub>GM</sub>	Peak gate power	aver any 20 manariad	-		20		W
P <sub>G(AV)</sub> T <sub>stg</sub> T <sub>j</sub>	Average gate power Storage temperature Operating junction temperature	over any 20 ms period	-40 -		0.5 150 125		ວໍວູ&

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15  $A/\mu s$ .

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## **ISOLATION LIMITING VALUE & CHARACTERISTIC**

 $T_{hs} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>isol</sub>	R.M.S. isolation voltage from all three terminals to external heatsink	f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65% ; clean and dustfree	-		2500	V
C <sub>isol</sub>	Capacitance from T2 to external heatsink	f = 1 MHz	-	10	-	pF

### THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-hs</sub>	Thermal resistance	with heatsink compound	-	-	4.0	K/W
$R_{thj-hs}$	Thermal resistance junction to heatsink	without heatsink compound	-	-	5.5	K/W
R <sub>th j-a</sub>	Thermal resistance junction to ambient	in free air	-	55	-	K/W

## STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

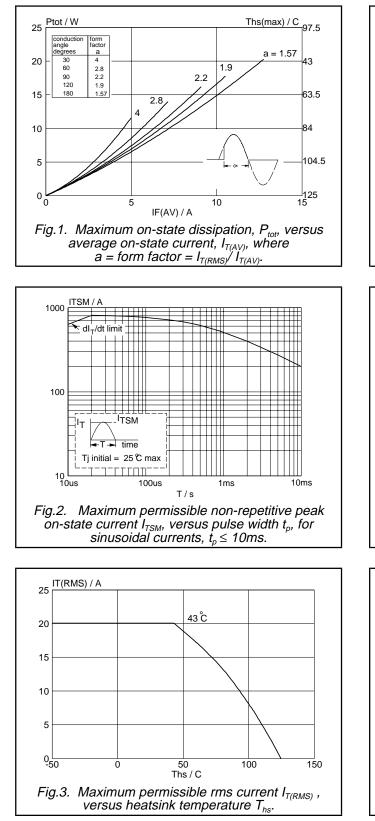
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>GT</sub>	Gate trigger current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$	-	3	32	mA
	Latching current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$	-	25	80	mA
I I <sub>H</sub>	Holding current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$	-	15	60	mA
ν, V <sub>T</sub>	On-state voltage	$I_{T} = 40 \text{ A}$	-	1.4	1.75	V
V <sub>GT</sub>	Gate trigger voltage	$\dot{V}_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$	-	0.6	1.5	V
0.		$V_{\rm D} = V_{\rm DRM(max)}; I_{\rm T} = 0.1 \text{ A}; T_{\rm i} = 125 \text{ °C}$	0.25	0.4	-	V
I <sub>D</sub> , I <sub>R</sub>	Off-state leakage current	$V_D = V_{DRM(max)}; V_R = V_{RRM(max)}; T_j = 125 \ ^{\circ}C$	-	0.2	1.0	mA

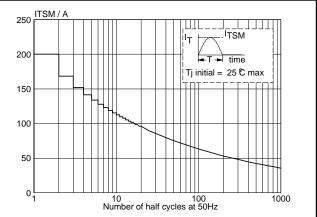
#### **DYNAMIC CHARACTERISTICS**

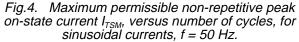
 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV <sub>D</sub> /dt	Critical rate of rise of	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$	200	300	-	V/µs
t <sub>gt</sub>	off-state voltage Gate controlled turn-on time	exponential waveform gate open circuit $V_D = V_{DRM(max)}$ ; $I_G = 0.1$ Å; $dI_G/dt = 5$ Å/µs; $I_{TM} = 40$ Å	-	2	-	μs
t <sub>q</sub>	Circuit commutated turn-off time	$V_{D}^{V_{D}} = 67\% V_{DRM(max)}; T_{j} = 125 °C;$ $I_{TM} = 50 A; V_{R} = 25 V; dI_{TM}/dt = 30 A/\mu s;$ $dV_{D}/dt = 50 V/\mu s; R_{GK} = 100 \Omega$	-	70	-	μs

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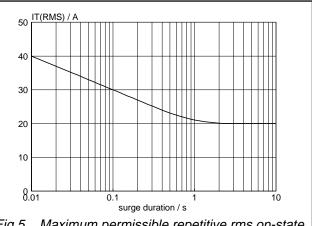
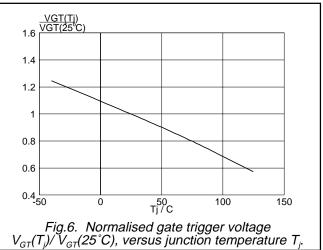
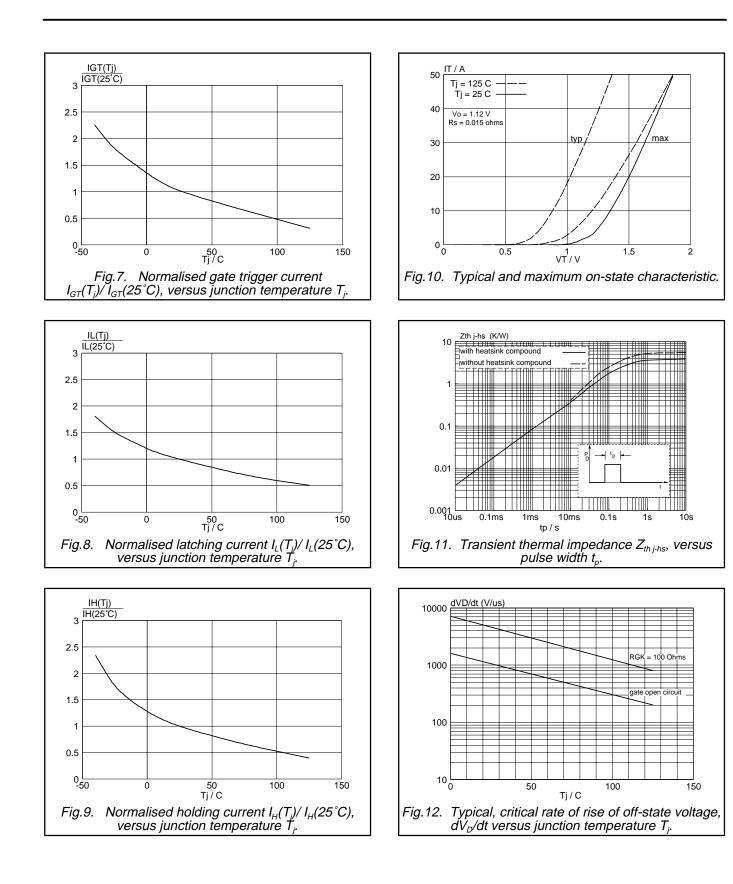


Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents, f = 50 Hz;  $T_{hs} \le 43$  °C.

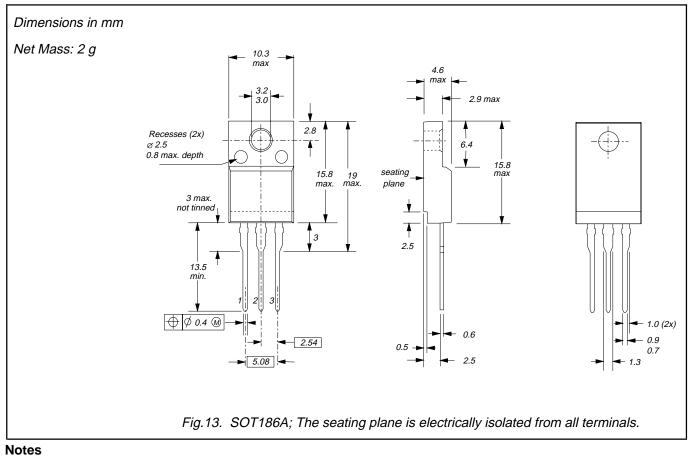


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#### **MECHANICAL DATA**



Refer to mounting instructions for F-pack envelopes.
Epoxy meets UL94 V0 at 1/8".

## DEFINITIONS

Data sheet status					
Objective specification	fication This data sheet contains target or goal specifications for product development.				
Preliminary specification	cation This data sheet contains preliminary data; supplementary data may be published later.				
Product specification	ct specification This data sheet contains final product specifications.				
Limiting values					
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.					
Application information					
Where application information is given, it is advisory and does not form part of the specification.					
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